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PATENT
(5500-88800/TT5375)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/676,437
Filed: October 1, 2003
Inventor(s):
Mitchell Alsup
Gregory William Smaus
James K. Pickett
Brian D. McMinn
Michael A. Fillippo
Benjamin T. Sander
Title: System And Method For
Handling Exceptional
Instructions In A Trace
Cache Based Processor

§ Examiner: Kim, Hong Chong
§ Group/Art Unit: 2185
§ Atty. Dkt. No: 5500-88800
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Robert C. Kowert

Printed Name

 Mar. 2, 2006
Signature Date


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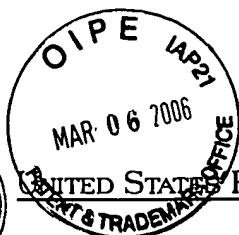
Sir:

The official Filing Receipt received by our office lists the Atty. Docket No. as 5500-91600. However, the correct Atty. Docket No. is 5500-88800. I have provided a copy of the Filing Receipt with the change noted thereon. Applicants respectfully request that this change be made and a corrected Filing Receipt be reissued in the above-identified application.

Respectfully submitted,


Robert C. Kowert
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/676,437	10/01/2003	2186	1036	5500-94600- 88800	7	30	4

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FILING RECEIPT



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Date Mailed: 12/23/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

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Assignment For Published Patent Application

Advanced Micro Devices, Inc., Sunnyvale, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/23/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

cc: AMD 1-8-04

Title

System and method for handling exceptional instructions in a trace cache based processor

Preliminary Class

711

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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